

IN THE CLAIMS

This listing of the claim will replace all prior versions and listings of claim in the present application.

Listing of Claims

Claims 1-16 (canceled).

17. (currently amended) ~~The~~ An information processing system ~~in accordance with claim 16,~~ comprising:

an input unit for receiving, from external of said information processing system, circuit information of a module constituting a semiconductor integrated circuit, a floorplan which is allocation information of blocks constituting the module, and evaluation indices, generated based on said circuit information of the module, for evaluating modifications of the floorplan,

wherein the circuit information, floorplan and evaluation indices are associated with each other;

a storing unit for storing the associated circuit information, floorplan and evaluation indices; and

a processing unit for reading the floorplan stored in the storing unit according to specification information for modifying the floorplan when the specification information is input, generating a plurality of floorplan candidates each being modified based on the read floorplan and the specification information, evaluating the generated floorplan candidates based on the evaluation indices stored in the storing unit, and selecting one floorplan based on an evaluation result,

wherein the evaluation indices ~~includes~~ include know-how of a designer who designs a semiconductor integrated circuit to be used in evaluating a generated floorplan.

Claim 18 (canceled).

19. (currently amended) ~~An~~ The information processing system ~~in accordance with claim 18,~~ used for designing a semiconductor integrated circuit, comprising:

a storing unit for storing circuit information of a module constituting the semiconductor integrated circuit, a floorplan which is allocation information of blocks constituting the module, and evaluation indices, generated based on said circuit information of the module, for evaluating modifications of the floorplan,

wherein the circuit information, floorplan and evaluation indices are associated with each other; and

an input/output unit for transmitting the associated circuit information, floorplan and evaluation indices from the storing unit,

wherein the evaluation indices ~~includes~~ include know-how of a designer who designs a semiconductor integrated circuit to be used in evaluating a generated floorplan.

Claim 20 (canceled).

Claim 21 (canceled).